

EMBEDDED SYSTEMS

The teacher, who is indeed wise, does not bid you to enter the house of his wisdom but rather leads you to the threshold of your mind.

—KHALIL GIBRAN

EMBEDDED SYSTEMS

An Integrated Approach



LYLA B DAS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY CALICUT
KOZHIKODE, KERALA

PEARSON

Chennai • Delhi • Chandigarh

Assistant Editor—Acquisitions: R. Vijay Pritha
Associate Editor—Production: M.R. Ramesh

Copyright © 2013 Dorling Kindersley (India) Pvt. Ltd

This book is sold subject to the condition that it shall not, by way of trade or otherwise, be lent, resold, hired out, or otherwise circulated without the publisher's prior written consent in any form of binding or cover other than that in which it is published and without a similar condition including this condition being imposed on the subsequent purchaser and without limiting the rights under copyright reserved above, no part of this publication may be reproduced, stored in or introduced into a retrieval system, or transmitted in any form or by any means (electronic, mechanical, photocopying, recording or otherwise), without the prior written permission of both the copyright owner and the publisher of this book.

ISBN 978-81-317-8766-3

First Impression

Published by Dorling Kindersley (India) Pvt. Ltd, licensees of Pearson Education in South Asia.

Head Office: 7th Floor, Knowledge Boulevard, A-8 (A), Sector 62, Noida 201 309, UP, India.
Registered Office: 11 Community Centre, Panchsheel Park, New Delhi 110 017, India.

Compositor: White Lotus Infotech Pvt. Ltd, Puducherry
Printed in India.

This book is dedicated
to my children
and
to all my students



CONTENTS

<i>Preface</i>	<i>xiii</i>
<i>About the Author</i>	<i>xix</i>

Part I Design Aspects of Embedded Systems **1**

0 Basics of Computer Architecture and the Binary Number System **3**

0.1	Basics of Computer Architecture	3
0.2	Computer Languages	8
0.3	RISC and CISC Architectures	10
0.4	Number Systems	11
0.5	Number Format Conversions	13
0.6	Computer Arithmetic	21
0.7	Units of Memory Capacity	30
	<i>Key Points of this Chapter</i>	31
	<i>Questions</i>	31
	<i>Exercises</i>	32

1 Introduction to Embedded Systems **34**

1.1	Application Domain of Embedded Systems	35
1.2	Desirable Features and General Characteristics of Embedded Systems	35
1.3	Model of an Embedded System	37
1.4	Microprocessor vs Microcontroller	37
1.5	Example of a Simple Embedded System	40
1.6	Figures of Merit for an Embedded System	41
1.7	Classification of MCUs: 4/8/16/32 Bits	42
1.8	History of Embedded Systems	44
1.9	Current Trends	45
	<i>Key Points of this Chapter</i>	45
	<i>Questions</i>	46
	<i>Exercises</i>	46

2 Embedded Systems—The Hardware Point of View **47**

2.1	Microcontroller Unit (MCU)	48
2.2	A Popular 8-bit MCU	50
2.3	Memory for Embedded Systems	64
2.4	Low Power Design	78
2.5	Pullup and Pulldown Resistors	79

	<i>Key Points of this Chapter</i>	84
	<i>Questions</i>	85
	<i>Exercises</i>	85
3	Sensors, ADCs and Actuators	86
3.1	Sensors	87
3.2	Analog to Digital Converters	97
3.3	Actuators	104
	<i>Key Points of this Chapter</i>	130
	<i>Questions</i>	131
	<i>Exercises</i>	132
4	Examples of Embedded Systems	133
4.1	Mobile Phone	133
4.2	Automotive Electronics	139
4.3	Radio Frequency Identification (RFID)	143
4.4	Wireless Sensor Networks (WISNET)	145
4.5	Robotics	146
4.6	Biomedical Applications	150
4.7	Brain Machine Interface	151
	<i>Key Points of this Chapter</i>	156
	<i>Questions</i>	156
	<i>Exercises</i>	157
5	Buses and Protocols	158
5.1	Defining Buses and Protocols	158
5.2	On-board Buses for Embedded Systems	166
5.3	External Buses	172
5.4	Automotive Buses	188
5.5	Wireless Communications Protocols	194
	<i>Key Points of this Chapter</i>	202
	<i>Questions</i>	203
	<i>Exercises</i>	203
6	Software Development Tools	204
6.1	Embedded Program Development	204
6.2	Downloading the Hex File to the Non-volatile Memory	211
6.3	Hardware Simulator	215
	<i>Key Points of this Chapter</i>	216
	<i>Questions</i>	216
	<i>Exercises</i>	217
Part II	Software Design Aspects	219
7	Operating System Concepts	221
7.1	Embedded Operating Systems	223
7.2	Network Operating Systems (NOS)	223

7.3	Layers of an Operating System	223
7.4	History of Operating Systems	224
7.5	Functions Performed by an OS (Components of an OS)	225
7.6	Some Terms Associated with Operating Systems and Computer Usage	230
7.7	The Kernel	231
7.8	Tasks/Processes	234
7.9	Scheduling Algorithms	239
7.10	Threads	250
7.11	Interrupt Handling	251
7.12	Inter Process (Task) Communications (IPC)	252
7.13	Task Synchronization	257
7.14	Semaphores	265
7.15	Priority Inversion	266
7.16	Device Drivers	268
7.17	Codes/Pseudo Codes for OS Functions	272
	<i>Key Points of this Chapter</i>	287
	<i>Questions</i>	287
	<i>Exercises</i>	288

8 Real-time Operating Systems 290

8.1	Real-time Tasks	290
8.2	Real-time Systems	294
8.3	Types of Real-time Tasks	294
8.4	Real-time Operating Systems	296
8.5	Real-time Scheduling Algorithms	298
8.6	Rate Monotonic Algorithm	302
8.7	The Earliest Deadline First Algorithm	306
8.8	Qualities of a Good RTOS	308
	<i>Questions</i>	309
	<i>Exercises</i>	309

9 Programming in Embedded C 311

9.1	Embedded C	311
9.2	PIC Programming Using MPLAB	328
	<i>Key Points of this Chapter</i>	331
	<i>Questions</i>	331
	<i>Exercises</i>	332

Part III Popular Microcontrollers Used in Embedded Systems 333

10 ARM—The World's Most Popular 32-bit Embedded Processor (Part I – Architecture and Assembly Language Programming) 335

10.1	History of the ARM Processor	335
10.2	ARM Architecture	344
10.3	Interrupt Vector Table	348

10.4	Programming the ARM Processor	349
10.5	ARM Assembly Language	349
10.6	ARM Instruction Set	352
10.7	Conditional Execution	356
10.8	Arithmetic Instructions	357
10.9	Logical Instructions	359
10.10	Compare Instructions	360
10.11	Multiplication	361
10.12	Division	362
10.13	Starting Assembly Language Programming	363
10.14	General Structure of an Assembly Language Line	364
10.15	Writing Assembly Programs	365
10.16	Branch Instructions	366
10.17	Loading Constants	370
10.18	Load and Store Instructions	375
10.19	Readonly and Read/Write Memory	381
10.20	Multiple Register Load and Store	382
	<i>Key Points of this Chapter</i>	389
	<i>Questions</i>	389
	<i>Exercises</i>	390

11 ARM—The World’s Most Popular 32-bit Embedded Processor (Part II – Peripheral Programming of ARM MCU Using C) 391

11.1	Block Diagram	392
11.2	Features of the LPC 214x Family	393
11.3	Peripherals	397
11.4	ARM 9	424
11.5	ARM Cortex-M3	424
	<i>Key Points of this Chapter</i>	427
	<i>Questions</i>	428
	<i>Exercises</i>	428

12 Cypress’s PSoC: A Different Kind of MCU 429

12.1	How to get a PSoC Development Kit	430
12.2	The PSoC Family	433
12.3	PSoC1	434
12.4	The Internal Architecture of PSoC	437
12.5	The Digital Sub System	443
12.6	GPIO Pins	453
12.7	Digital Applications Using PSoC	456
12.8	The Analog Section	463
12.9	System Resources	473
12.10	PSoC3 and PSoC5	476
	<i>Key Points of this Chapter</i>	477
	<i>Questions</i>	478
	<i>Exercises</i>	479

13	The 8051 Microcontroller: The Programmer's Perspective	480
13.1	History and Family Details of 8051	480
13.2	8051: The Programmer's Perspective	482
13.3	Assembly Language Programming	485
13.4	Internal RAM	491
13.5	The 8051 Stack	493
13.6	Processor Status Word (PSW)	495
13.7	Assembler Directives	496
13.8	Storing Data in Code Memory (ROM)	497
13.9	The Instruction Set of 8051	499
13.10	Port Programming	514
13.11	Subroutines (Procedures)	520
13.12	Delay Loops	522
	<i>Key Points of this Chapter</i>	527
	<i>Questions</i>	527
	<i>Exercises</i>	528
14	Programming the Peripherals of 8051	529
14.1	Pin Configuration of 8051	529
14.2	Programming the Internal Peripherals	533
14.3	Timers of 8051	535
14.4	Counter Programming	545
14.5	Interrupts of 8051	548
14.6	Serial Communication	558
	<i>Key Points of this Chapter</i>	565
	<i>Questions</i>	565
	<i>Exercises</i>	566
15	DSP Processors	567
15.1	The Application Scenario	568
15.2	General Features of Digital Signal Processors	569
15.3	SIMD Techniques	581
15.4	The SHARC Floating Point Processor	587
15.5	DSP Processors of Texas Instruments (TI)	590
15.6	OMAP (Open Multimedia Applications Platform)	592
	<i>Key Points of this Chapter</i>	594
	<i>Questions</i>	595
	<i>Exercises</i>	595
Part IV	Design and Performance Aspects	597
16	Automated Design of Digital ICs	599
16.1	History of Integrated Circuit (IC) Design	599
16.2	Types of Digital ICs	599

16.3	ASIC Design	605
16.4	ASIC Design: The Complete Sequence	609
	<i>Key Points of this Chapter</i>	612
	<i>Questions</i>	612
	<i>Exercises</i>	612
17	Hardware Software Co-design and Embedded Product Development Lifecycle Management	613
17.1	Hardware Software Co-design	614
17.2	Modelling of Systems	616
17.3	Embedded Product Development Lifecycle Management	620
17.4	Lifecycle Models	626
	<i>Key Points of this Chapter</i>	629
	<i>Questions</i>	629
	<i>Exercises</i>	629
18	Embedded Design: A Systems Perspective	630
18.1	A Typical Example	631
18.2	Product Design	633
18.3	The Design Process	637
18.4	Testing	654
18.5	Bulk Manufacturing	655
	<i>Key Points of this Chapter</i>	657
	<i>Questions</i>	657
	<i>Exercises</i>	658
Part V	Projects	659
19	Academic Projects	661
19.1	Project No: 1	661
19.2	Project No: 2	675
19.3	Project No: 3	683
	<i>Key Points of this Chapter</i>	693
	<i>Questions</i>	693
	<i>Exercises</i>	694
	Appendix A	695
	Appendix B	700
	Appendix C	710
	Appendix D	729
	Bibliography	741
	Index	745

PREFACE

Preamble

Writing a book on Embedded Systems is not easy—let me list a few reasons to substantiate this statement. The first reason is that the field of embedded systems is very vast. The second is that there is no clear understanding on what exactly a student of engineering should learn about embedded systems. A great number of products which are classed as embedded systems are available, and the field is very sophisticated, well developed and rapidly expanding. Anything from a printer to an iPhone is an embedded system. To write a book on all this is quite difficult on account of not having a clear idea of where to start and where to end.

To complicate matters further, there are different families of embedded processors. A student cannot be expected to learn all of them, or even some of them. To make a decision on what to include and what not to, has been difficult. Besides that, ‘embedded processors’ is not the only topic to learn. There is a large set of various kinds of sensors, actuators, buses, operating systems, design methodologies, view points, development models and what not.

But after a lot of contemplation, finally, I converged on a few popular and upcoming processors, latest buses, new approaches, traditional as well as modern peripherals, real time operating systems and the like. A lot of literature for all these is available in the form of technical documents, data sheets and user manuals—right from the USB technical spec to PSoC’s data sheets

Rummaging through all these highly sophisticated technical information, trying to make sense of it all, and finally presenting it in a way that a student, albeit an eager and enthusiastic one, will be able to enjoy reading and studying it—this is the challenge involved in writing this book. I have tried my best to address this challenge of making it a student-friendly presentation.

There are a number of books available under the title of ‘Embedded Systems’. Except for a few, most of them have simply concentrated on the architecture and application details of one particular processor. Others have concentrated on the software aspects alone. There are certain others that deal with both, but since the field of embedded systems is one in which fast evolution is the rule rather than the exception, some topics become outdated quite fast.

Approach

I have started from the hardware basics, proceeded to discuss some important processors and systems, and then moved on to the software aspects. The book ends with a presentation on embedded design from a system point of view. Along with the basics, I have also tried to focus on the latest and most relevant topics in the field, from the latest processors and buses to the latest trends in embedded computing.

Pre-requisite

A student of CS, EC or EE branch who has done a first course in digital logic and a second course in ‘microprocessors and microcontrollers’, is best placed to take up a course on Embedded Systems.

But it is possible also to study Embedded Systems as a second course—that is why some very basic ideas of microprocessors and microcontrollers are included in the initial chapters.

Organization of the Book

This book is organized as twenty chapters numbered from 0 to 19. It is divided into five logical parts from Part I to Part V.

Part I

This part, which includes chapters 0 to 6, deals with the basics, the hardware aspects including sensors, actuators, buses etc. and the tools commonly used in system development.

Chapter 0 is a revision of computer arithmetic and computer architecture. One needs to be very thorough in these two basic topics—then the path ahead becomes very comfortable.

Chapter 1 introduces readers to what an embedded system is, and what its mandatory parts are. Examples of practical and popularly used embedded systems are listed to make the introduction clear. The classifications, history and current trends in the embedded industry are also touched upon.

Chapter 2 is a very important chapter—any student who needs to use/learn embedded hardware should become conversant and confident about all the topics covered in this chapter. Not only are the important aspects of typical embedded processors covered here, related topics such as semiconductor memory (RAM and Flash), low power design, concepts of pullup and pulldown resistors are also touched upon.

Chapter 3 is very important for practical design of systems. Most students are likely to do hardware based projects as part of academic requirements—this chapter, which gives an in-depth discussion on sensors and actuators will definitely find use then.

Chapter 4 is meant for a light reading on some of the applications of embedded systems. Mobile phones, robotics, RFIDs, automotive electronics, medical electronics etc. are discussed as popular applications. A new idea called ‘brain machine interface’ is also introduced in this chapter.

Chapter 5 is meant to be studied as a very important topic. It contains explanations of some of the popular buses used in embedded systems. A student is not expected to study all buses in detail, but a general idea of buses, and a study of some of the important ones is advised. On-board and off-board buses, wired and wireless buses, bus standards, bus arbitration etc are the important topics covered here.

Chapter 6 is a brief introduction to the development tools that are needed to take a project to completion. The discussion is meant to guide students in the right direction when they are confused about the techniques for writing programs, testing them and burning them into hardware.

Part II

This is the second part of the book, and there are three chapters here. This part deals with software design aspects. Chapter 7 is quite lengthy, but it should mandatorily be learned because it gives answers to many aspects of computers and embedded systems that are seen and experienced in everyday life. This chapter covers operating system concepts in detail and then offers codes/pseudo codes where OS concepts are tried out.

Chapter 8 is about ‘real time operating systems’. This should be considered as a continuation of the previous chapter, But here the special requirements and scheduling policies for a special class of embedded systems i.e., real time systems, are taken up. Numerical problems are worked in both these chapters to understand the scheduling mechanism used in operating systems.

Chapter 9 is a short chapter, being at best a basic introduction to Embedded C. It assumes that the reader has a basic knowledge of the constructs of ‘C’. How this high-level language is used for processor programming is the focus of the discussion, which is based on the 8051 architecture. Some codes for PIC are also included. Later chapters of PSoC and ARM contain more coding using Embedded C, but basic ideas are introduced here, in Chapter 9.

Part III

This part consists of Chapter 10 to Chapter 15. The architecture, programming and applications of some of the most widely used and popular processors are covered in reasonable depth.

Chapters 10 and 11 are devoted to the ARM processor, which is the most popular processor used in 32-bit and high-end applications. Chapter 10 explains the core of ARM and follows it up with assembly language programming. Chapter 11 expands ARM architecture to make it a microcontroller. A specific ARM-based MCU is chosen and its peripherals are studied. Programming of some peripherals using C is done. These two chapters are likely to be sufficient to get a good grip on ARM architecture.

Chapter 12 is about a new processor. It is not new in the embedded design world, but the academic world is just getting familiarized with it – the chapter discusses PSoC, an MCU series, which makes life easier for a product designer. This is because of the graphical IDE it has, and other special features that are covered (with programming examples in Embedded C) in the chapter.

Chapters 13 and 14 are about one of the most widely used 8-bit microcontroller i.e., the 8051. This MCU is simple and the first that a student should study. These two chapters discusses this MCU with assembly language programming. All the peripherals are covered, and programming is explained with worked-out examples.

Chapter 15 contains a general coverage of DSP processors. Such processors are increasing in relevance and the time is just right to learn the special features of such chips. The special features of such processors are first explained, and then some popular DSP processors (BlackFin, SHARC, OMAP etc) have been identified, and their features elaborated.

Part IV

This part includes Chapters 16, 17 and 18.

Chapter 16 deals with ASIC design. It starts with the classification of digital ICs, continues with programmable devices and then gives a step-by-step explanation of how a digital IC is designed, tested and fabricated. The reader can get a good idea of what terms such as front-end design, back-end design etc. mean, without going very deep into the process of ASIC design.

Chapter 17 introduces two new terminology. One is 'Hardware Software Co-design' and the other is 'Embedded Product Development Lifecycle'. Both these terms are explained and elaborated upon, with relevant examples.

Chapter 18 is very special. After all the previous chapters, it looks upon an embedded product as a system, and suggests the steps needed to apply embedded systems to make useful products as demanded by users. The user/users might have their viewpoint expressed before the design starts. New concepts like user research, ergonomics, anthropometry etc are introduced. Starting from the desires of users, the design steps reach the final stage of product manufacture and resting.

Part V

This part has just one chapter. Chapter 19 has a concise discussion of three projects done by students. The projects pertain to embedded hardware and software and use advanced processors—ARM, OMAP and PIC. This chapter is meant to encourage students to take up challenging and innovative ideas and build products based on these ideas.

Appendices

The book has appendices from A to K. Only Appendix A to D are in the text book, The rest are available in the website of the book www.pearsoned.co.in/lylabdas/embeddedsystems.

The contents of the appendices are as listed:

- A - The instruction set of 8051
- B - A step-by-step guide to using the Keil RVDK for 8051 and ARM
- C - A step-by-step guide to using the PSoC Designer
- D - Pin configuration and PINSEL register configuration of LPC 2148
- E - A manual with experiments for PSoC1
- F - A step by step guide to using PSoC Creator
- G - A tutorial on Keil RVDK for 8051 and ARM
- H - A program for interfacing a Graphical LCD to PSoC3
- I - A program for interfacing an SD card to ARM7 (LPC 2148)
- J - A program for using the I2HC interface of PSoC1
- K - User manual of ARM LPC 2148

In addition, PowerPoint presentations and solution manual of the chapters are available for instructors.

Contact

Your suggestions and feedback are welcome. In spite of my best efforts, it is possible that some errors may have crept in. Please point them out to me.

My contact id is lbd@nitc.ac.in

ACKNOWLEDGEMENTS

This is my second major book, and as I complete it, I would like to acknowledge all those who have helped and encouraged me in this Herculean task. Truly, it has been a great effort to write it, and there are a lot of people who have directly or indirectly helped me. Let me start from the beginning.

The team at Pearson Education provided a lot of inputs, suggestions and support and brought the project to fruition. I feel that Sojan Jose, my editor, and Ramesh M. R and Vijay Pritha, the production editors have done a tremendous job.

The first batch of students I taught Embedded Systems was the B070EC batch and the next year, the B080EC batch came in. Both batches expressed enthusiasm and interest in the topics I taught, and this is the primary factor that gave me the courage to embark on the venture of writing a book on the subject. I would like to thank each and every one of them for this.

During the process of writing, a few students helped me directly in bringing the book to this form. They gave suggestions, performed reviews, and three of them have contributed by writing a few sections of the book. All of them are working in reputed companies and I would like to list their names, along with expressing my heartfelt thanks to them.

Nithin Gopinath (Texas Instruments, Bangalore), Sabu Paul (Texas Instruments, Bangalore) and Sai Krishna K. (Broadcom, Bangalore) are the three who have contributed directly by writing a few sections in the book.

The list of those who have done reviews of the chapters are: Nithin Gopinath (Texas Instruments, Bangalore), Jayalal Vijayan (Synopsis, Bangalore), Sai Krishna K. (Broadcom, Bangalore), Harikrishnan M. (McAfee, Bangalore), Srijit R. (Deloitte, Hyderabad) and Sushmitha Dandeliya (Assistant Professor, Engineering College, Gwalior).

A few of my colleagues also have helped me in this endeavor, and I extend my gratitude to them. Raghu C. V., my colleague in the department, has done the writing of Chapter 18, and has also given me suggestions at various stages of this work. The names of others with whom I have had discussions on some topics are Sameer S. M., Deepthi P. P., Sudheesh George, Bhuvan B. and Rajiv T. R., my department colleagues, and Jayaraj B. and Anu Mary Chacko of the Computer Science Department. I am grateful to Anand, senior mechanic at the Embedded Systems Lab, who assisted me in all the hardware work associated with the book. I thank Beljit, Anju and Aswathi who have drawn the diagrams in the book. I would also like to make a note of acknowledgement to my son Sagar, for his suggestions on the theme of the front cover of the book.

Two engineers at Cypress Semiconductors, Narayana Swamy, and Geethesh N. S., made a detailed review of the chapter on PSoC. Their inputs have enhanced the quality of the chapter. I am obliged to them and also to Benoy Jose, and Karthikeyan Mahalingam of Cypress Semiconductors for co-ordinating this activity.

It is only because my department gave me free time without the hassles of regular work that I have been able to complete the book on schedule. All my colleagues have been helpful in this and I feel that words are not sufficient to express my feelings of gratitude to all of them. I am deeply indebted to my institute for giving me the freedom to grow and follow the path I chose.

Chapter 19 of the book contains the project work of a few teams of students. They have worked systematically and enthusiastically to do projects of good standard, which require a lot of background study. I congratulate them for the work they have done and would like to mention their names here. They are: Nithin Gopinath, Jayalal Vijayan, Ashwin Harikumar, Kurian Abraham, Ebin George, Sushmita Dandeliya, Fahim Bin Basheer, Jinu J. Alias, Mohammed Favas C., Navas V. and Naveed Farhan K.

I am happy that my family has always been a source of solace for me.

Last, but not the least, I thank all my students once again for the inspiration they have always been, and continue to be.

LYLA B. DAS

ABOUT THE AUTHOR

Lyla B. Das is Associate Professor, Department of Electronics Engineering, National Institute of Technology Calicut (NITC), Kerala. She has a diverse mix of industrial, teaching and research experience spanning about 30 years. As a young graduate specializing in Electronics and Communications from the College of Engineering, Trivandrum, Lyla B. Das joined Keltron Controls as Deputy Engineer in 1981. She joined NITC (then Regional Engineering College, Calicut), as lecturer in 1985 and proceeded to complete her master's degree in digital communications from the same college. Over the years, she was successively elevated as Assistant Professor and then Associate Professor, a position which she currently holds.

Keen to actively seek and impart knowledge, Lyla B. Das currently teaches courses on microprocessors, microcontrollers, digital system design using VHDL, and system design using embedded processors at the undergraduate as well as postgraduate level. She has presented research papers in conferences of national and international stature and has worked on numerous projects based on microprocessors and microcontrollers, such as microprocessor-based voting machines and microcontroller-based rail track switching system. An avid reader of contemporary research material, she keeps herself abreast of the current trends in her chosen field and guides students in their M. Tech. research theses. This book on *Embedded Systems* is her second book with Pearson Education, the first one being *The X86 Microprocessors*, which was published in 2010 and received with wide acclaim.

Lyla B. Das has worked on various projects funded by the ministry of human resource development (MHRD) in thrust areas of growth including the setting up of an embedded systems laboratory in 2005–2008. She has delivered expert lectures on image compression using wavelets, advanced microprocessors and microcontrollers, FPGA based systems and embedded systems at several engineering colleges across Kerala. She has also participated in numerous tutorials and workshops conducted by the Indian Institute of Technology (IIT) and the Indian Institute of Science (IISc). She was a Fellow in the national conference on 'VLSI Design and Embedded Systems' held at IISc Bangalore (2003) and IIT Mumbai (2004). She is a life member of the System Society of India and a member of the Indian Society for Technical Education and the Computer Society of India.

