

NATIONAL INSTITUTE OF TECHNOLOGY CALICUT

Walk in Interview

A Walk-in-interview for the appointment of a **Research Assistant** (on contract basis) for a Project in the Department of Electronics and Communication Engineering, NIT Calicut is scheduled to be held on **28.12.2018** at 9.30 a.m.at the Department of Electronics and Communication Engineering, National Institute of Technology Calicut-673601.

Name of Project: SMDP C2SD (DeitY) Scheme: Special Manpower Development Chip to System Design – Department of Electronics and Information Technology, MIT, Govt. of India.

Name of the sponsoring Agency : DeitY

Duration of Appointment : **One year** (on contract basis)

Consolidated monthly Fellowship : Rs.20,000/- (Rupees twenty thousand only) per month

Qualification & Experience : An M.Tech./M.E. Degree in Microelectronics / VLSI from a reputed Institute, with a CGPA of atleast 6.5 on a 10-point scale.

Desirable qualification : In-depth knowledge of custom/semicustom EDA tool flow from schematic design to GDS II generation (schematic design, simulation, layout, parasitic extraction, post layout simulation and GDS II generation).

Eligible candidates may report on **28.12.2018 at 9.30 a.m.** at the Department of Electronics and Communication Engineering, NIT Calicut, NIT Campus (P.O), Calicut 673 601, for a walk-in-interview with Bio-data and original certificates and with any other relevant documents (and one set of xerox). No travelling or any other allowances is admissible for attending the interview.

**Sd/-
DEAN (R&C)**