



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF II SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, APRIL 2018**

**II Semester M.Tech/M.Plan in Microelectronics & Vlsi Design**

SI No	Reg. No.	EC6207		EC6208		EC6132		EC6224		EC6104		EC6227		EC6125		EC6209		EC6210		EC6211						SGPA
		MOS DEVICE MODELING (3)		VLSI SYSTEM DESIGN (3)		ADVANCED EMBEDDED COMPUTING (3)		TESTING and VERIFICATION OF VLSI CIRCUITS (3)		DSP SYSTEM DESIGN (3)		LOW POWER VLSI (3)		HIGH SPEED DIGITAL DESIGN (3)		MICRO ELECTRONIC LAB II (2)		VLSI DESIGN LAB (2)		SEMINAR (1)						
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
1	M170182EC	A	N	B	H			C	H			B	H	C	H	S	H	A	H	B	H					8.15
2	M170070EC	A	H	A	H	S	N	A	H			S	H			A	H	A	H	A	H					9.3
3	M170141EC	B	N	C	H	A	N	D	H			B	H			A	H	A	H	A	H					7.95
4	M170251EC	B	H	C	H			C	N			B	H	D	H	A	H	A	H	A	H					7.65
5	M170111EC	C	H	D	H			D	N			C	N	D	N	A	H	B	H	B	H					6.9
6	M170074EC	W	L	W	L			W	L			W	L	W	L	W	L	W	L	W	L					0
7	M170051EC	W	L	W	L			W	L			W	L	W	L	W	L	W	L	W	L					0
8	M170275EC	S	H	A	H			A	H	S	H	A	H			S	H	S	H	S	H					9.55
9	M170459EC	A	H	C	H			B	H			A	N	C	H	A	H	A	H	A	H					8.25
10	M170083EC	B	H	B	H			B	H			B	N	C	N	A	H	A	H	A	H					8.1
11	M170225EC	D	N	E	N			E	N			E	N	E	N	C	N	B	H	C	N					5.75
12	M170144EC	A	H	A	H	A	N	A	H			A	H			S	H	A	H	A	H					9.1
13	M170241EC	S	H	S	H	S	N	B	H			S	H			S	H	S	H	S	H					9.7
14	M170172EC	C	N	C	H			C	H			C	N	D	N	A	H	B	H	B	H					7.2
15	M170197EC	B	N	B	H			B	H			B	H	D	N	A	H	B	H	B	H					7.8



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF II SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, APRIL 2018**

**II Semester M.Tech/M.Plan in Microelectronics & Vlsi Design**

SI No	Reg. No.	EC6207		EC6208		EC6132		EC6224		EC6104		EC6227		EC6125		EC6209		EC6210		EC6211				SGPA		
		MOS DEVICE MODELING (3)		VLSI SYSTEM DESIGN (3)		ADVANCED EMBEDDED COMPUTING (3)		TESTING and VERIFICATION OF VLSI CIRCUITS (3)		DSP SYSTEM DESIGN (3)		LOW POWER VLSI (3)		HIGH SPEED DIGITAL DESIGN (3)		MICRO ELECTRONIC LAB II (2)		VLSI DESIGN LAB (2)		SEMINAR (1)						
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att		Grade	Att
16	M170142EC	D	N	E	N	D	N	E	H			D	N			B	N	C	H	B	H					6.1
17	M170185EC	A	H	B	H			B	H			B	H	B	N	S	H	S	H	A	H					8.6
18	M170073EC	A	H	A	H	S	N	A	H			S	H			A	H	A	H	A	H					9.3

Chairman - Class Committee

Professor & Head,  
Department of Electronics Engineering