



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF II SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, APRIL 2018**

**II Semester M.Tech/M.Plan in Electronics Design And Technology**

SI No	Reg. No.	EC6104		EC6105		EC6132		EC6208		EC6224		EC6227		EC6108		EC6106		EC6107						SGPA	
		DSP SYSTEM DESIGN (3)		ELECTROMAGNETIC COMPATIBILITY (4)		ADVANCED EMBEDDED COMPUTING (3)		VLSI SYSTEM DESIGN (3)		TESTING and VERIFICATION OF VLSI CIRCUITS (3)		LOW POWER VLSI (3)		SEMINAR (1)		ELECTRONIC S SYSTEM DESIGN LAB (2)		DSP LAB (2)							
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att		Grade
1	M170534EC	D	H	C	H			D	N	E	N	C	H	D	H	A	H	B	H						6.67
2	M170157EC	C	H	C	H	B	N			D	H	C	H	D	H	B	H	C	H						7.05
3	M170263EC	A	N	A	N	B	N	B	N	B	N			D	H	E	H	A	H						8.05
4	M170440EC	D	N	D	N			E	N	E	N	D	N	D	H	A	H	B	H						6.19
5	M170390EC	A	H	A	H			A	N	B	N	A	N	B	H	B	H	A	H						8.71
6	M170581EC	B	N	C	H	D	N			C	N	C	N	D	H	C	H	A	H						7.14
7	M170069EC	A	H	B	H	A	N	B	H	B	H			C	H	B	H	A	H						8.33
8	M170229EC	C	N	B	H	C	N			D	H	C	H	D	H	C	H	B	H						7.1
9	M170391EC	C	H	C	N			E	N	C	N	A	N	B	H	C	H	B	H						7.14
10	M170179EC	B	N	B	H			D	N	B	N	D	N	D	H	C	H	B	H						7.24
11	M170504EC	F	N	F	N	F	N			F	N	F	H	D	H	E	H	D	H						1.33
12	M170549EC	B	H	A	H	A	N	B	N	A	N			C	H	A	H	A	H						8.62
13	M170049EC	D	H	E	N	B	N			E	N	E	N	D	H	C	H	B	H						6.1
14	M170409EC	S	H	S	H			B	H	B	H	B	H	C	H	A	H	A	H						8.81
15	M170445EC	C	H	D	H	C	N	E	H	E	H			D	H	C	H	B	H						6.29



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		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att		Grade	Att
16	M170412EC	C	N	C	N	B	N			D	N	B	H	C	H	B	H	B	H							7.33
17	M170455EC	A	H	A	H	A	N	B	H	B	H			C	H	B	H	S	H							8.62

Chairman - Class Committee

Professor & Head,  
Department of Electronics Engineering