



NATIONAL INSTITUTE OF TECHNOLOGY CALICUT
DEPARTMENT OF ELECTRONICS ENGINEERING

RESULTS OF III SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2018

III Semester M.Tech/M.Plan in Microelectronics & Vlsi Design

SI No	Reg. No.	EC7201																								SGPA
		PROJECT WORK (8)																								
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att			
1	M170182EC	B	H																						8	
2	M170070EC	B	H																						8	
3	M170141EC	S	H																						10	
4	M170251EC	B	H																						8	
5	M170111EC	C	H																						7	
6	M170275EC	S	H																						10	
7	M170459EC	B	H																						8	
8	M170083EC	B	H																						8	
9	M170225EC	D	N																						6	
10	M170144EC	A	H																						9	
11	M170241EC	S	H																						10	
12	M170172EC	A	H																						9	
13	M170197EC	B	H																						8	
14	M170142EC	B	H																						8	
15	M170185EC	B	H																						8	



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16	M170073EC	A	H																						9	

Chairman - Class Committee

Professor & Head,
 Department of Electronics Engineering