



NATIONAL INSTITUTE OF TECHNOLOGY CALICUT
DEPARTMENT OF ELECTRONICS ENGINEERING

RESULTS OF III SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2018

III Semester M.Tech/M.Plan in Electronics Design And Technology

SI No	Reg. No.	EC7101																								SGPA
		PROJECT WORK (8)																								
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att			
1	M170534EC	C	H																						7	
2	M170157EC	S	H																						10	
3	M170263EC	B	H																						8	
4	M170440EC	C	H																						7	
5	M170390EC	S	H																						10	
6	M170581EC	B	H																						8	
7	M170069EC	A	H																						9	
8	M170229EC	B	H																						8	
9	M170391EC	A	H																						9	
10	M170179EC	A	H																						9	
11	M170549EC	A	H																						9	
12	M170049EC	C	H																						7	
13	M170409EC	B	H																						8	
14	M170445EC	A	H																						9	
15	M170412EC	B	H																						8	



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16	M170455EC	A	H																						9	

Chairman - Class Committee

Professor & Head,
 Department of Electronics Engineering