



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF III SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2017**

**III Semester M.Tech/M.Plan in Electronics Design And Technology**

SI No	Reg. No.	EC7101																								SGPA
		PROJECT WORK (8)																								
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att			
1	M160267EC	A	H																						9	
2	M160482EC	W	L																						0	
3	M160431EC	B	H																						8	
4	M160381EC	S	H																						10	
5	M160073EC	A	H																						9	
6	M160219EC	B	H																						8	
7	M160342EC	C	H																						7	
8	M160447EC	C	H																						7	
9	M160528EC	S	H																						10	
10	M160120EC	B	H																						8	
11	M160260EC	B	H																						8	
12	M160117EC	S	H																						10	
13	M160226EC	C	H																						7	
14	M160294EC	A	H																						9	
15	M160417EC	A	H																						9	



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		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att			
16	M160112EC	C	H																						7	
17	M160334EC	W	L																						0	
18	M150204EC	B	N																						8	

Chairman - Class Committee

Professor & Head,  
 Department of Electronics Engineering