



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF I SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2017**

**I Semester M.Tech/M.Plan in Microelectronics & Vlsi Design**

SI No	Reg. No.	EC6201		EC6202		EC6203		EC6204		EC6101		EC6205		EC6206										SGPA
		BASICS OF VLSI (4)		SEMICONDUCTOR DEVICE THEORY AND MODELLING (3)		VLSI TECHNOLOGY (3)		ANALOG INTEGRATED CIRCUIT DESIGN (3)		DIGITAL SYSTEM DESIGN (4)		MICRO ELECTRONIC LAB I (2)		SYSTEM DESIGN USING HDL (2)										
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
1	M170182EC	B	H	A	H	S	N	C	H	A	H	S	N	B	H									8.67
2	M170070EC	A	H	B	H	B	N	B	H	S	H	A	N	A	H									8.76
3	M170141EC	C	H	D	H	B	N	C	H	B	H	C	N	A	H									7.38
4	M170251EC	B	H	S	H	B	N	E	H	D	H	A	N	B	H									7.57
5	M170111EC	C	N	D	H	B	N	E	N	B	N	A	N	A	H									7.29
6	M170074EC	B	H	A	H	B	N	B	N	D	H	B	N	B	H									7.76
7	M170051EC	C	H	A	H	C	N	B	N	C	H	B	N	D	H									7.43
8	M170275EC	A	H	A	H	S	N	B	H	B	H	S	N	B	H									8.81
9	M170459EC	B	H	S	H	A	N	B	N	B	H	S	N	A	H									8.71
10	M170083EC	B	H	A	H	B	N	B	N	A	H	B	N	A	H									8.43
11	M170225EC	I	N	E	N	I	N	I		E	N	B	N	D	N									3
12	M170144EC	S	H	A	H	C	N	B	H	A	H	A	N	B	H									8.67
13	M170241EC	S	H	S	H	S	N	B	H	S	H	A	N	S	H									9.62
14	M170172EC	C	N	C	H	D	N	E	N	D	H	C	N	B	H									6.48
15	M170197EC	C	H	C	H	B	N	C	N	A	H	A	N	A	H									7.9



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		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
16	M170142EC	E	N	C	H	C	N	E	N	C	N	B	N	B	H									6.52
17	M170185EC	A	H	B	H	A	N	B	H	B	H	S	N	B	H									8.52
18	M170073EC	A	H	B	H	A	N	C	H	A	H	B	N	A	H									8.48
19	M170188EC	F	N	F	H	F	N	F	N	F	N	D	N	E	N									1.05

Chairman - Class Committee

Professor & Head,  
Department of Electronics Engineering