



NATIONAL INSTITUTE OF TECHNOLOGY CALICUT
DEPARTMENT OF ELECTRONICS ENGINEERING

RESULTS OF I SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2018

I Semester M.Tech/M.Plan in Microelectronics & Vlsi Design

SI No	Reg. No.	EC6201D		EC6202D		EC6203D		EC6102D		EC6103D		EC6101D												SGPA
		DIGITAL INTEGRATED CIRCUIT DESIGN (4)		ANALOG INTEGRATED CIRCUIT DESIGN (4)		ADVANCED SEMICONDUCTOR DEVICE MODELING (4)		EMBEDDED SYSTEM DESIGN (4)		MATHEMATICAL TECHNIQUES FOR ELECTRONICS DESIGN (4)		DIGITAL SYSTEM DESIGN USING HDL (4)												
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
1	M180240EC	B	H	A	H	A	H	A	H			B	H											8.6
2	M180231EC	B	N	B	H	B	H	C	N			C	H											7.6
3	M180113EC	B	H	B	H	B	N			A	H	B	H											8.2
4	M180118EC	C	H	D	H	C	N			C	H	D	H											6.6
5	M180135EC	D	H	D	H	D	N			C	N	C	H											6.4
6	M180041EC	A	N	B	H	B	H	B	N			A	H											8.4
7	M180173EC	D	H	E	H	D	H			D	H	F	H											4.6
8	M180046EC	B	H	C	H	B	H	C	H			A	H											7.8
9	M180075EC	C	H	C	H	C	H			C	H	C	H											7
10	M180167EC	A	H	A	H	A	H	B	H			C	H											8.4
11	M180241EC	B	H	B	H	C	H	B	H	A	H													8
12	M180111EC	A	H	S	H	S	H			S	H	A	H											9.6
13	M180115EC	C	N	C	H	C	N	C	H			C	H											7
14	M180082EC	A	H	B	H	A	H			A	H	A	H											8.8
15	M180345EC	B	H	B	H	S	H			A	H	C	H											8.4



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		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
16	M180208EC	C	N	C	H	B	N			A	H	B	H											7.8
17	M180043EC	C	N	B	H	A	H			B	H	D	H											7.6
18	M180449EC	B	H	A	H	A	H			S	H	B	H											8.8

Chairman - Class Committee

Professor & Head,
 Department of Electronics Engineering