



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF I SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2018**

**I Semester M.Tech/M.Plan in Signal Processing**

SI No	Reg. No.	EC6401D		EC6301D		EC6402D		EC6428D		EC6432D														SGPA
		LINEAR ALGEBRA FOR SIGNAL PROCESSING (4)		RANDOM PROCESSES (4)		DIGITAL SIGNAL PROCESSING ALGORITHMS (4)		NEURAL NETWORKS (3)		MULTIRATE SIGNAL PROCESSING AND FILTER BANKS (3)														
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
1	M180308EC	C	H	A	H	B	H	A	H	C	H													8
2	M180352EC	A	H	C	H	C	H	B	H	C	H													7.61
3	M180131EC	E	H	E	H	E	H	E	H	E	N													5
4	M180412EC	A	H	A	H	A	H	A	H	A	H													9
5	M180555EC	S	H	B	H	B	H	B	H	A	N													8.61
6	M180441EC	A	H	B	H	C	H	C	H	C	H													7.67
7	M180419EC	D	H	B	H	D	H	B	H	B	H													7.11
8	M180416EC	B	H	C	H	B	H	B	H	C	H													7.61
9	M180556EC	A	H	C	H	A	H	A	H	A	H													8.56
10	M180401EC	W	L	W	L	W	L	W	L	W	L													0
11	M180551EC	C	H	D	H	C	H	C	H	E	H													6.44
12	M170458EC			D	N																			6



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**Chairman - Class Committee**

**Professor & Head,  
Department of Electronics Engineering**