



**NATIONAL INSTITUTE OF TECHNOLOGY CALICUT**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**

**RESULTS OF I SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2018**

**I Semester M.Tech/M.Plan in Electronics Design And Technology**

SI No	Reg. No.	EC6101D		EC6102D		EC6103D		EC6201D		EC6132D		EC6203D												SGPA
		DIGITAL SYSTEM DESIGN USING HDL (4)		EMBEDDED SYSTEM DESIGN (4)		MATHEMATICAL TECHNIQUES FOR ELECTRONICS DESIGN (4)		DIGITAL INTEGRATED CIRCUIT DESIGN (4)		DATA STRUCTURES AND ALGORITHMS (3)		ADVANCED SEMICONDUCTOR DEVICE MODELING (4)												
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
1	M180426EC	E	H	C	H	B	H	B	N	D	H													6.84
2	M180375EC	D	H	C	H	B	H	E	N			B	N											6.8
3	M180309EC	S	H	A	H	S	H	C	H	A	H													9
4	M180388EC	A	H	S	H	A	H	B	H	A	H													9
5	M180439EC	C	H	C	H	A	H	A	H	B	H													8
6	M180287EC	B	H	B	H	A	H	D	N	A	H													7.95
7	M180310EC	C	H	S	H	C	H	D	H	C	H													7.42
8	M180138EC	E	H	D	N	B	H	E	H	C	H													6.16
9	M180339EC	A	H	A	H	S	H	B	H	B	H													8.84
10	M180464EC	B	H	C	H	C	H	D	N			C	N											7
11	M180494EC	F		W	L	W	L	W	L	W	L													0
12	M180372EC	B	H	A	H	A	H	D	N	A	H													8.16
13	M180302EC	C	H	C	H	B	H	C	N	B	H													7.37
14	M180430EC	C	H	C	H	B	H	D	H	B	H													7.16
15	M180418EC	D	H	D	N	C	N	D	N	C	H													6.37



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		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
16	M180298EC	B	H	C	H	S	H	C	N	A	H													8.16
17	M180374EC	A	H	A	H	A	H	B	H	S	H													8.95
18	M180048EC	C	H	D	H	E	H	D	H	C	H													6.16

Chairman - Class Committee

Professor & Head,  
 Department of Electronics Engineering