



NATIONAL INSTITUTE OF TECHNOLOGY CALICUT
DEPARTMENT OF ELECTRONICS ENGINEERING

RESULTS OF I SEMESTER M.TECH/M.PLAN DEGREE EXAMINATION, NOVEMBER 2017

I Semester M.Tech/M.Plan in Electronics Design And Technology

SI No	Reg. No.	EC6101		EC6102		EC6201		EC6103		EC6202		EC6204														SGPA
		DIGITAL SYSTEM DESIGN (4)		EMBEDDED SYSTEM DESIGN (4)		BASICS OF VLSI (4)		ANALOG AND DATA CONVERSION SYSTEMS (4)		SEMICONDUCTOR DEVICE THEORY AND MODELLING (3)		ANALOG INTEGRATED CIRCUIT DESIGN (3)														
		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
1	M170534EC	C	H	A	H	B	H	B	H	A	H															8.16
2	M170157EC	D	H	B	H	D	H	D	H	E	H															6.26
3	M170263EC	A	N	S	H	B	N	B	H			B	H													8.63
4	M170535EC	W	L	W	L	W	L	W	L	W	L															0
5	M170440EC	D	H	C	H	D	H	C	H	D	H															6.42
6	M170390EC	A	N	A	H	B	N	B	H	A	H															8.58
7	M170581EC	D	N	A	H	D	N	I	N	I	N															4.42
8	M170069EC	A	H	B	H	B	H	A	H			C	N													8.26
9	M170229EC	C	H	A	H	D	H	D	N	C	H															7
10	M170391EC	B	N	A	N	B	N	B	H	A	H															8.37
11	M170179EC	C	H	B	H	B	N	C	H	B	H															7.58
12	M170504EC	E	H	E	H	E	H	F	H	D	H															4.11
13	M170549EC	A	H	S	H	B	H	B	H	C	H															8.47
14	M170049EC	C	N	C	H	D	H	E	H	E	H															6.05
15	M170409EC	A	H	S	H	A	H	S	H	S	H															9.58



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		Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	Grade	Att	
16	M170445EC	C	H	B	H	D	H	D	H	E	H													6.47
17	M170412EC	C	H	A	H	D	H	D	H	A	H													7.32
18	M170455EC	C	H	S	H	B	H	A	H			C	N											8.26

Chairman - Class Committee

Professor & Head,
 Department of Electronics Engineering